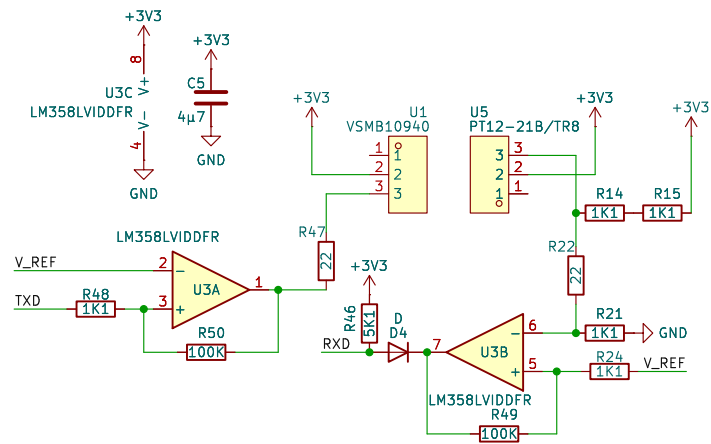


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TXD TXD
RXD RXD
VREFD V_REF
SUPPLYD +3V3
GNDD GND



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